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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant:

Jeffrey R. Wilcox, et al.

2187

Serial No.:

10/038,960

amended)

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Examiner:

Glenn A. Gossage

Filed:

January 2, 2002

Atty. Dkt. No.:

ITL.0668US (P12986)

For:

Method and Apparatus For Reducing Power Consumption In A Memory Bus Interface By Selectively Disabling And Enabling Sense Amplifiers (as

Ally. DKI. No..

Group Art Unit:

RECEIVED

MAY 2 7 2004

Technology Center 2100

Mail Stop RCE
Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449 together with any required copies of such references. This statement is being filed with the Request For Continued Examination. Please apply any charges or credits to Deposit Account No. 20-1504. (ITL.0668US)

Date: May 20, 2004

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Date of Deposit: May 20, 2004

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Janice Munøz

(Use several mecessary)

ATTY DOCKET NO. SERIAL NO. ITL.0668US (P12986) 10/038,960 APPLICANT(S):

Jeffrey R. Wilcox, et al. FILING DATE:

January 2, 2002

GROUP ART UNIT:

2187

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
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		Technology Center 2100					
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO	
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
•	U.S. Patent Application Serial No. 10/317,776, filed December 11, 2002, entitled "An Apparatus and						
		Method For Address Bus Power Control" U.S. Patent Application Serial No. 10/317,798, filed December 11, 2002, entitled "An Apparatus and					
	Method For Address E	Method For Address Bus Power Control"					
	U.S. Patent Application	U.S. Patent Application Serial No. 10/436,903, filed May 12, 2003, entitled "An Apparatus and Method For					
	Address Bus Power C	Address Bus Power Control"					
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EXAMINER		DATE CONSIDERED					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy							

of this form with next communication to applicant.